Express Mailing Label No. EL815829555 US
Docket No. 00SC053US8

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: M. Loose

Examiner: C. Kao

Serial No. 09/671,409

Art Unit: 2882

Filing Date: September 27, 2000

For: IMAGER WITH ADJUSTABLE RESOLUTION

Assistant Commissioner for Patents
Box Non-Fee Amendment
Washington, D.C. 20231

AMENDMENT TRANSMITTAL

Sir:

Transmitted herewith is an Amendment for the Office Action dated September 19, 2002.

If any fee is required, charge Account No. 18-1750. A duplicate of this transmittal is attached.

Respectfully submitted,

December 12, 2002

Steven C. Patrick
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M:\S2-00SC053US8\Transmittal

CERTIFICATE OF MAILING

I hereby certify that this correspondence is being deposited with the United States Postal Service Express Mailing Label No. EL815829555 US addressed to: Assistant Commissioner of Patents, Washington, D.C. 20231 on

12/12/02
Date

Marianne Middleton
Marianne Middleton

PATENT
00SC053US3

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: M. Loose
Serial No. 09/671,409
Filed: September 27, 2000
Examiner: C. Kac
Art Unit: 2882
Title: IMAGER WITH ADJUSTABLE RESOLUTION

Assistant Commissioner for Patents
Box Non-Fee Amendment
Washington, D. C. 20231

AMENDMENT

Sir:

In response to the Office Action dated September 19, 2002, kindly amend the above application as follows:

Claims

Please amend claims 1, 9 and 15 as follows:

1. (twice amended) A photodetector array comprising a plurality of addressable active pixels, each pixel comprising:

at least two photodiodes arranged such that their outputs may be switchably connected to a common pixel node;

a switching circuit which allows switching of at least one of said photodiodes between a first circuit and a second circuit;

wherein said first circuit directly combines the outputs of said at least two photodiodes in parallel, and said second circuit directly combines the output of said at least one of said photodiodes in parallel with the output of a photodiode of a neighboring pixel in the array, whereby said array is switchable between a high resolution and a low resolution pixel configuration, said pixel having an intrinsic capacitance which stores said combined photodiode outputs prior to their being read out, and